

<b>Notice of References Cited</b>	Application/Control No. 10/823,244		Applicant(s)/Patent Under Reexamination GAU, JING-HORNG	
	Examiner Johannes P. Mondt		Art Unit 3663	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-			
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#### NON-PATENT DOCUMENTS

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	U	Wolf, S, "Silicon Processing for the VLSI Era", Volume 2 - Process Integration, Lattice Press, Sunset beach, California 90742 (ISBN: 0-961672-4-5) (1990), pages 658-661.
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